

FIG.1

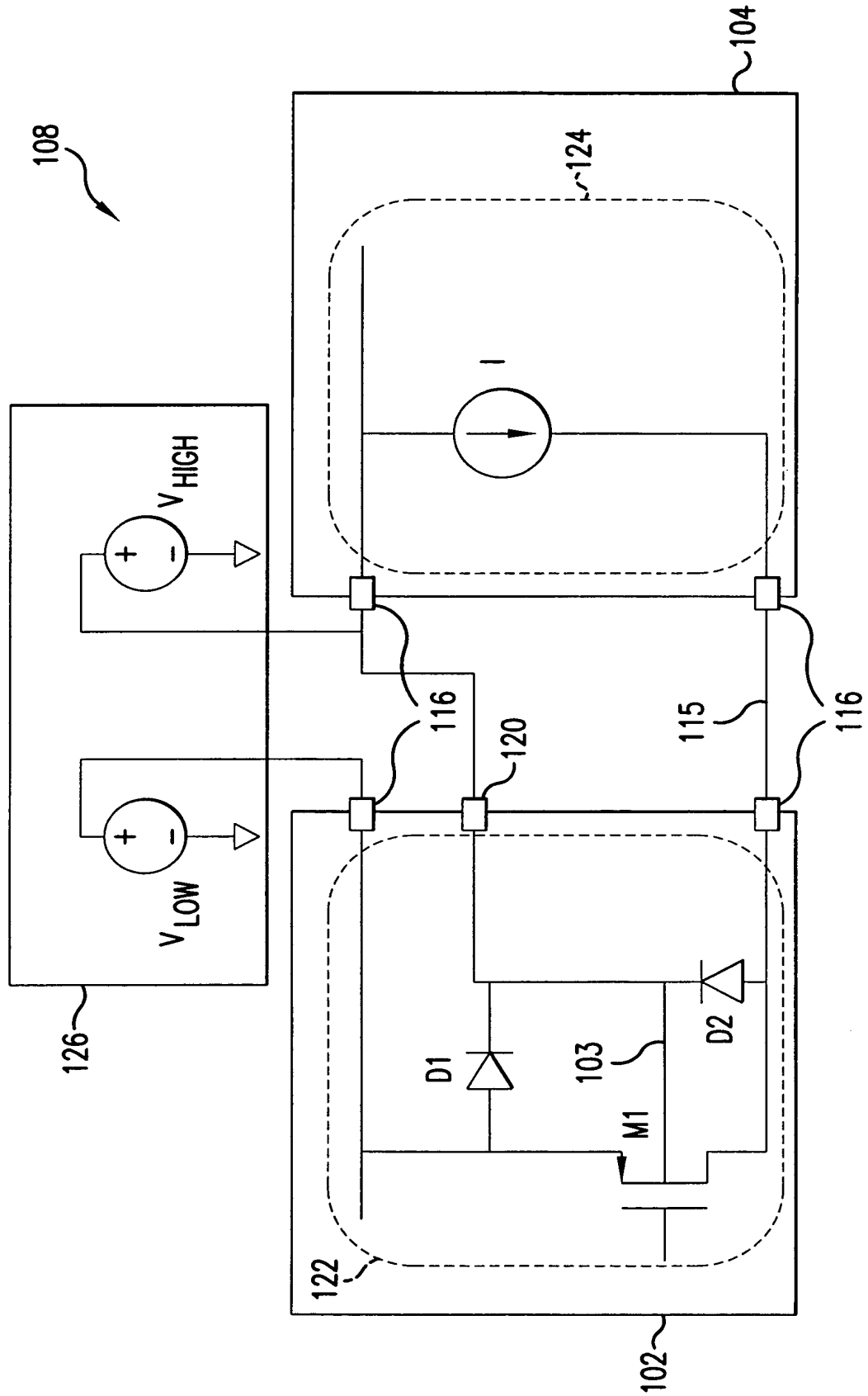
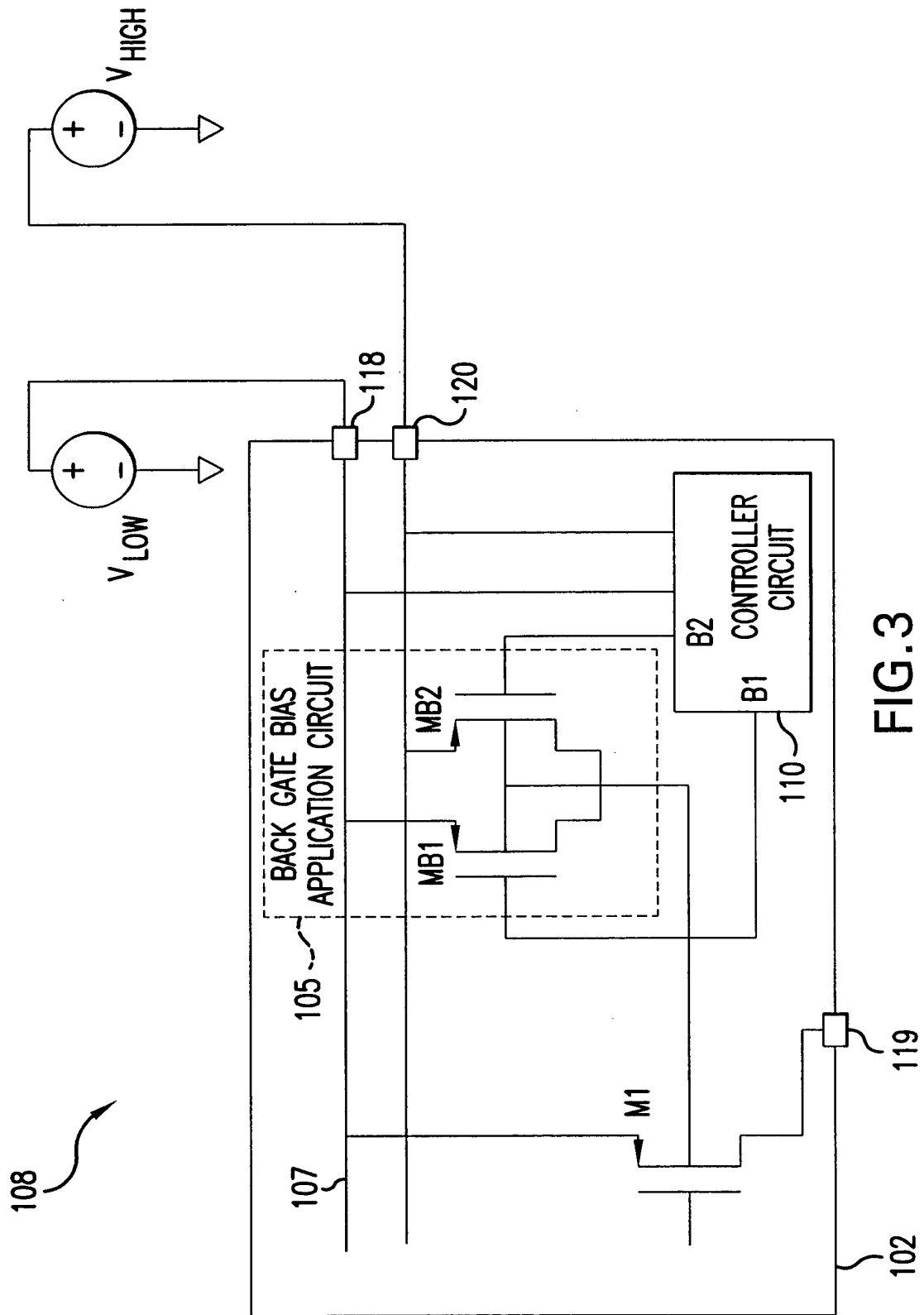


FIG.2

Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1875.0940003; Group Art Unit: *To Be Assigned*
 Inventor: Agnes N. Woo; Tel: 202/371-2600
 Title: System and Method for Independent Power
 Sequencing of Integrated Circuits



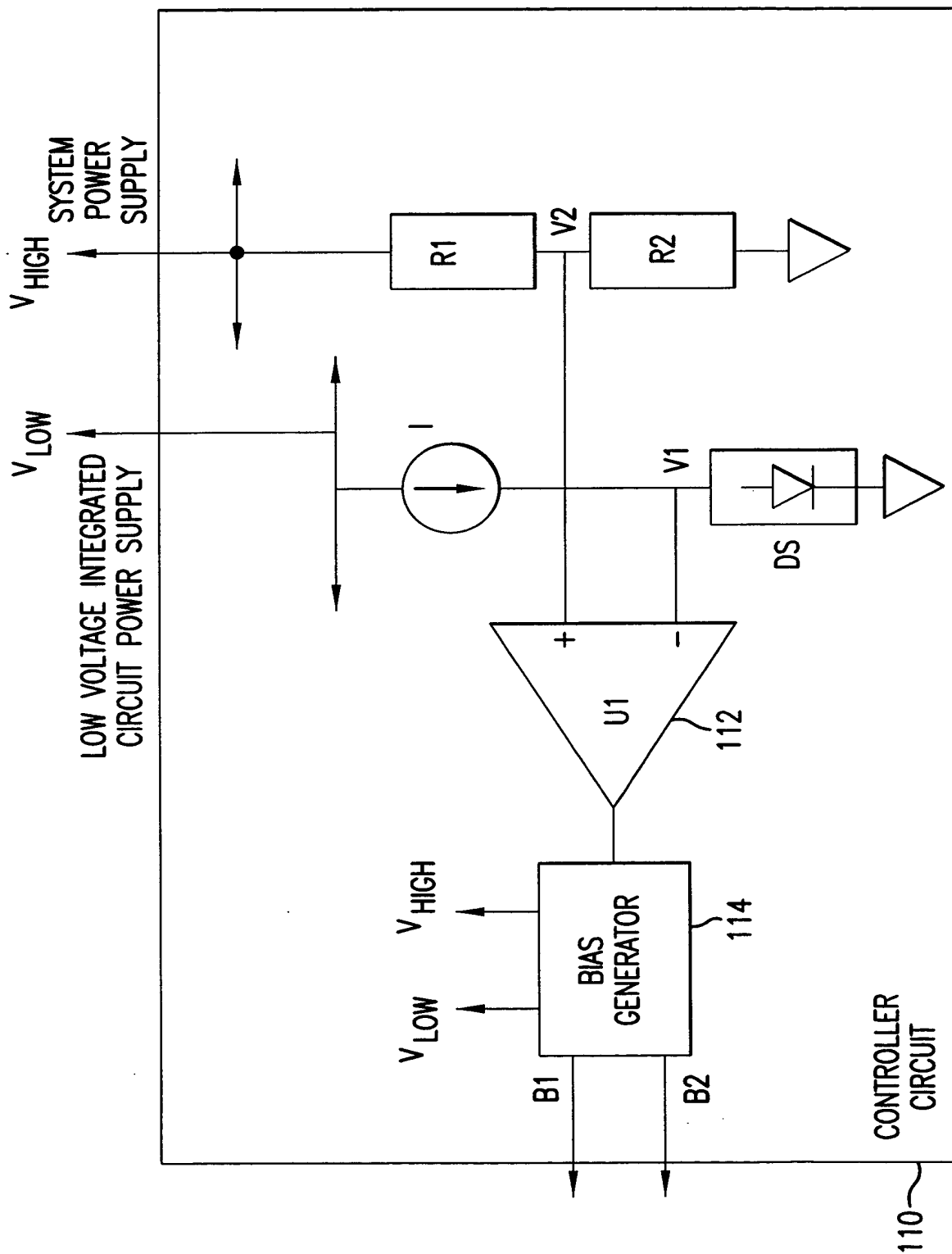


FIG.4

Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1875.0940003; Group Art Unit: *To Be Assigned*
 Inventor: Agnes N. Woo; Tel: 202/371-2600
 Title: System and Method for Independent Power
 Sequencing of Integrated Circuits

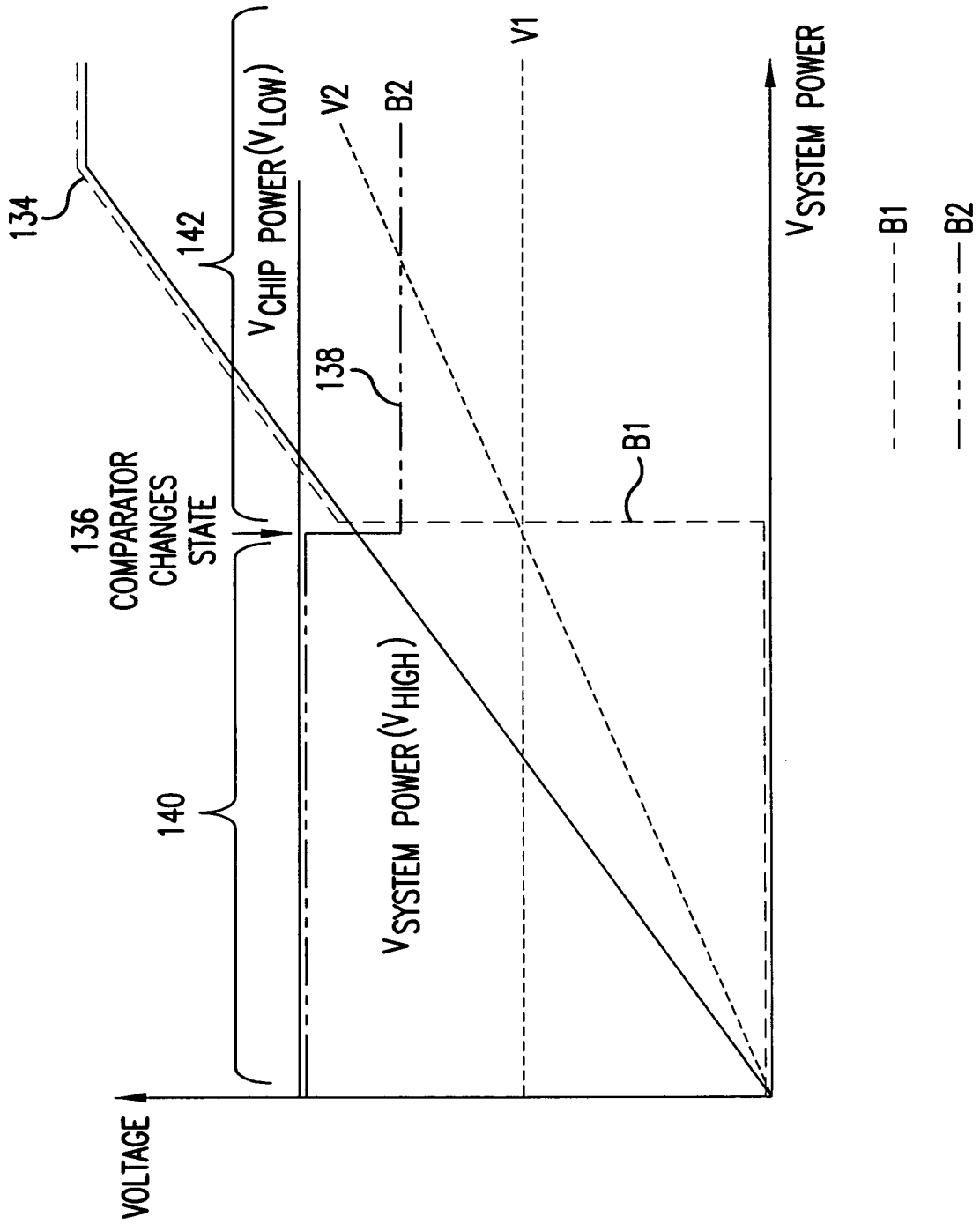


FIG. 5

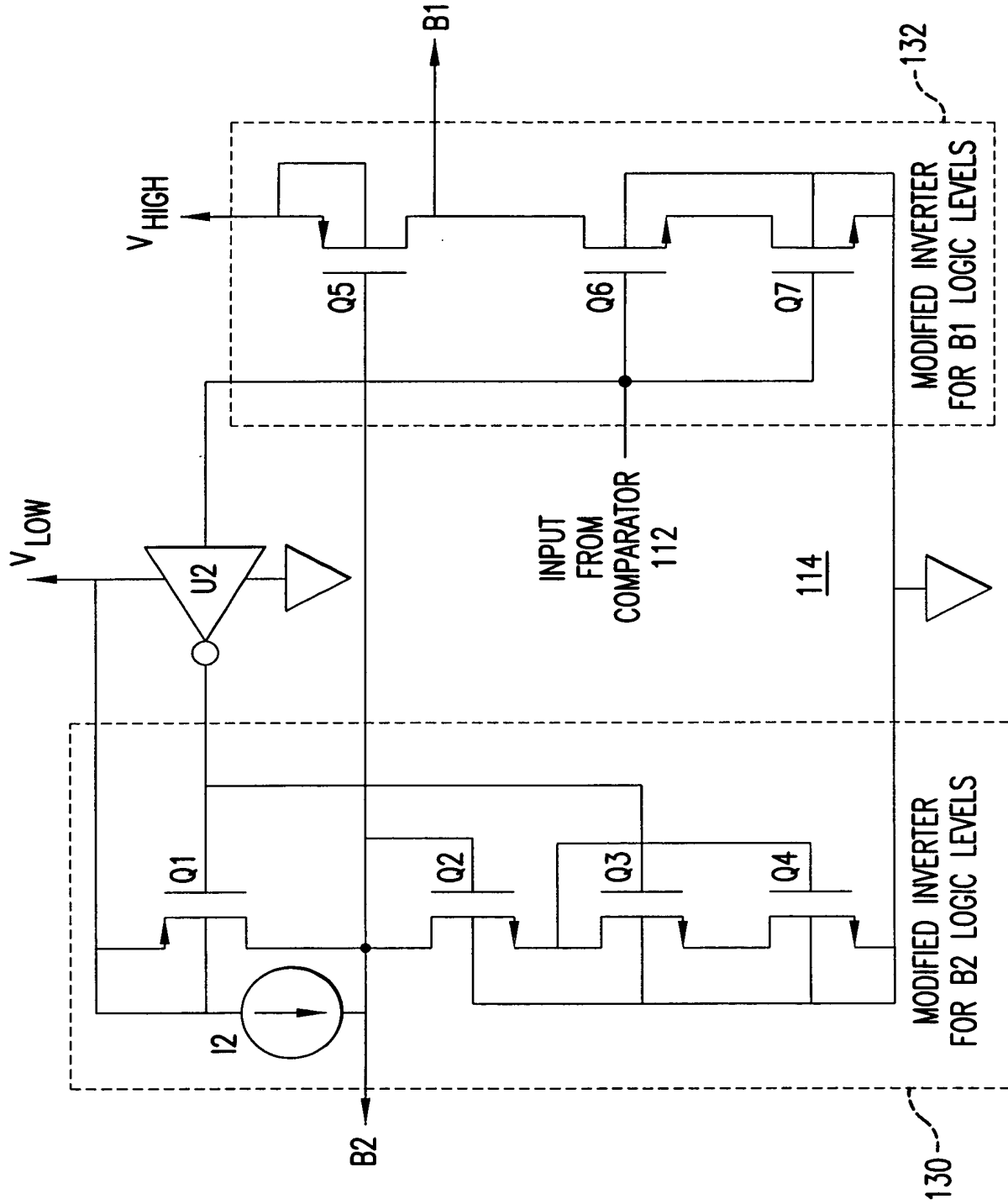


FIG.6

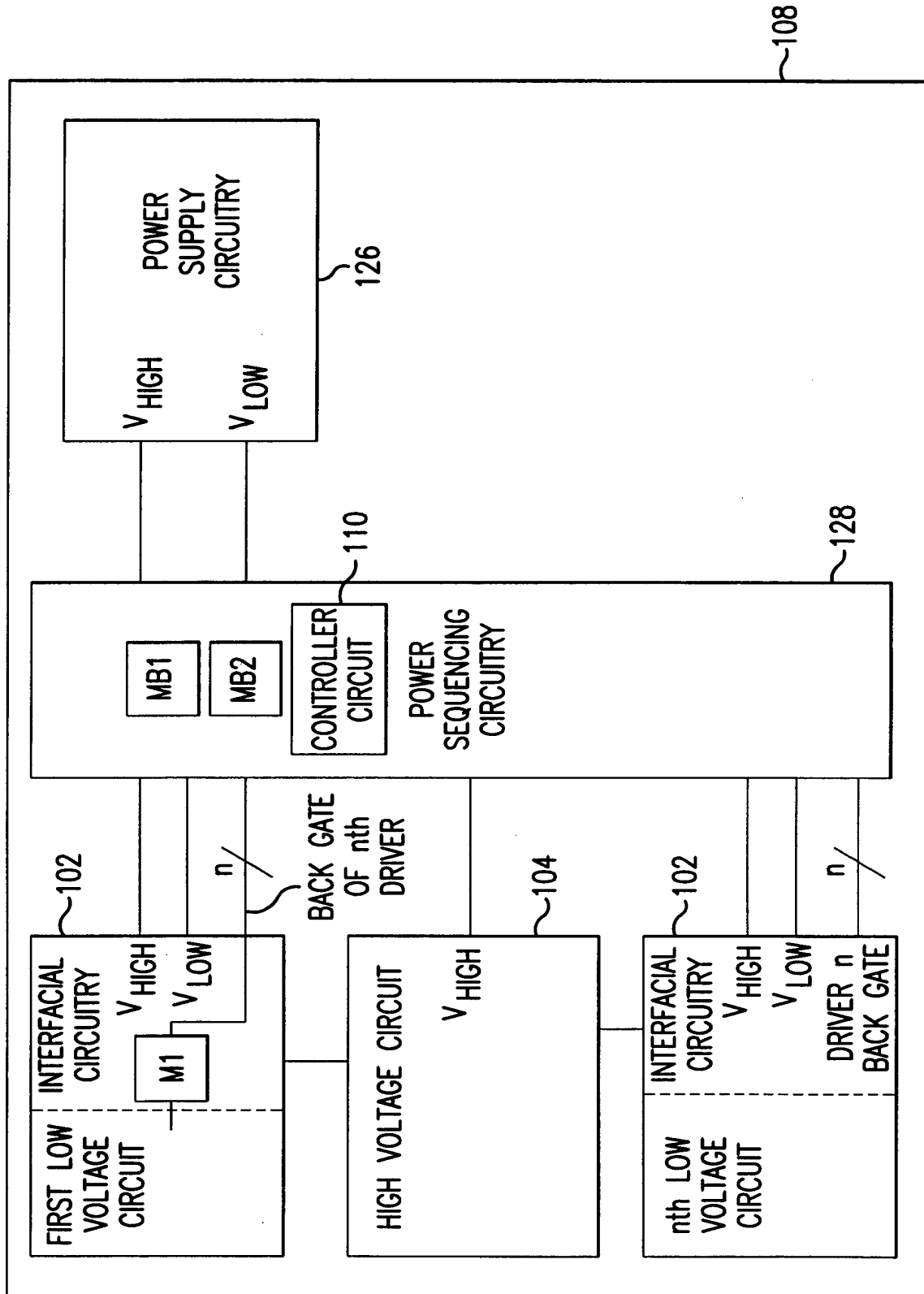


FIG. 7